

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	26	"three phase buffer"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 14:07
S2	9	"three phase buffers"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 14:07
S3	6420	("three phase" "tri-state" "three state" "three-phase") NEAR2 (buffer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:08
S4	6096	("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:09
S5	3856	((("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)) AND (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:09
S6	3251	((("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)) AND ((bus) WITH (internal external input output))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:10
S7	2262	((("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)) AND ((bus) WITH (internal external input output)) AND (cpu processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:11
S8	2170	((("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)) AND ((bus) WITH (internal external input output)) AND (cpu processor) AND (memory storage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:12

S9	1777	((("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)) AND ((bus) WITH (internal external input output)) AND ((cpu processor) with (memory storage)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:12
S10	1293	((("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)) AND ((bus) near2 (internal external input output)) AND ((cpu processor) with (memory storage)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:14
S11	880	((("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)) AND ((bus) near2 (internal external input output)) AND ((cpu processor) with (memory storage)) AND (port))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:14
S12	448	((("three phase" "tri-state" "three state" "three-phase") ADJ2 (buffer)) AND ((bus) near2 (internal external input output)) AND ((cpu processor) with (memory storage)) AND ((port) NEAR2 (peripheral data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 11:32
S13	10213	((tri three) NEAR2 (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 11:32
S14	1550	((tri three) NEAR2 (bus)) SAME (CPU microprocessor micro-processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 11:33
S15	837	((tri three) NEAR2 (bus)) SAME (CPU microprocessor micro-processor) SAME (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 11:34
S16	133	((tri three) NEAR2 (bus)) SAME (CPU microprocessor micro-processor) SAME (memory) SAME (buffer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 12:17

S17	3511	710/104,305,306,311,316.CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 12:22
S18	2344	710/104,305,306,311,316.CCLS. AND ((memory) SAME (cpu processor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 12:23
S19	1614	710/104,305,306,311,316.CCLS. AND ((memory) SAME (cpu processor) SAME (input output peripheral))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 12:25
S20	262	710/104,305,306,311,316.CCLS. AND ((memory) SAME (cpu processor) SAME (input output peripheral) SAME (buffer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 12:25
S21	6	("5930523" "6401197" "6411984"). pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 13:07
S22	2688	(memory) adj2 (connected) adj2 ("input/output" "i/o" output)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:06
S23	214	bus.ab. AND ((memory) adj2 (connected) adj2 ("input/output" "i/o" output))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:53
S24	4	bus.ab. AND ((memory) adj2 (connects) adj2 ("input/output" "i/o" output))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:56

S25	19	bus.ab. AND ((memory) adj2 (connect) adj2 ("input/output" "i/o" output))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:54
S26	10430	(microprocessor "micro-chip" microship) NEAR2 (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:57
S27	5556	((microprocessor "micro-chip" microship) NEAR2 (bus)) SAME (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:57
S28	1862	((microprocessor "micro-chip" microship) NEAR2 (bus)) SAME ((memory) WITH (input output "i/o" "input/output" port))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:59
S29	947	((microprocessor "micro-chip" microship) NEAR2 (bus)) SAME ((memory) NEAR4 (input output "i/o" "input/output" port))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 16:00
S30	811	((microprocessor "micro-chip" microship) NEAR2 (bus)) SAME ((memory) NEAR4 (input output "i/o" "input/output"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 16:01
S31	185	((microprocessor "micro-chip" microchip) NEAR2 (bus)) SAME (((memory) NEAR4 (input output "i/o" "input/output"))) WITH (connect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 17:10
S32	187	((microprocessor "micro-chip" microchip micro-controller) NEAR2 (bus)) SAME (((memory) NEAR4 (input output "i/o" "input/output"))) WITH (connect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 17:12

S33	38041	((microprocessor "micro-chip" microchip micro-controller) with (bus)).ab"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 17:12
S34	38041	((microprocessor "micro-chip" microchip micro-controller) with (bus)).ab"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 17:13
S35	5535	((microprocessor "micro-chip" microchip micro-controller) with (bus)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 17:13
S36	931	((microprocessor "micro-chip" microchip micro-controller) with (bus)).ab. AND ((memory) NEAR2 (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 17:13
S37	450	((microprocessor "micro-chip" microchip micro-controller) with (bus)).ab. AND ((memory) NEAR2 (bus)) AND ((external output input internal) NEAR2 (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 17:14
S38	244	((microprocessor "micro-chip" microchip micro-controller) with (bus)).ab. AND ((memory) NEAR2 (bus)) AND ((external output) NEAR2 (bus)) AND ((input internal) NEAR2 (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 11:04
S39	336	((microprocessor "micro-chip" microchip micro-controller) WITH (data internal) WITH (bus)).ab. AND ((external output) NEAR2 (bus)) AND ((input internal) NEAR2 (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 11:05
S40	191	((microprocessor "micro-chip" microchip micro-controller) WITH (data internal) WITH (bus)).ab. AND ((output) NEAR2 (bus)) AND ((input) NEAR2 (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 11:06

S41	25	((microprocessor "micro-chip" microchip micro-controller) WITH (internal) WITH (bus)).ab. AND ((output) NEAR2 (bus)) AND ((input) NEAR2 (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 11:49
S42	128	712/33.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:13
S43	2903	(memory) WITH (test) WITH (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:01
S44	979	Microprocessor AND ((memory) WITH (test) WITH (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:02
S45	95755	Microprocessor.ab. ((memory) WITH (test) WITH (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:02
S46	201	Microprocessor.ab. AND ((memory) WITH (test) WITH (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:44
S47	2038	(latch\$2) NEAR1 (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 14:45
S48	135	(Microprocessor) SAME ((latch\$2) NEAR1 (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 14:45

S49	9	(microprocessor).ab. AND ((three) NEAR (buses)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:15
S50	1024	(microprocessor).ab. AND ((buses)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:15
S51	410	((microprocessor).ab. AND (buses).ab.) AND ((memory) WITH (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:16
S52	58	((microprocessor).ab. AND (internal).ab. AND (buses).ab.) AND ((memory) WITH (bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:16
S53	5	((tri-bus) AND (microprocessor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:46
S54	2	((tri-bus) AND (microprocessor)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:44
S55	540	((internal) NEAR2 (bus)) AND (microprocessor)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:49
S56	368	((internal) NEAR2 (bus)) AND (microprocessor)).ab. AND (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:47

S57	383	((internal) NEAR2 (bus)) AND (microprocessor)).ab. AND (memory storage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:48
S58	182	(uni-directional) NEAR (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 16:23
S59	1245	"dual-port memory"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/05 14:21
S60	52	"dual-port memory" SAME (microprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/05 14:56
S61	1	"4626985".PN.	USPAT; USOCR	OR	OFF	2005/11/05 14:39
S62	1	"4984195".PN.	USPAT; USOCR	OR	OFF	2005/11/05 14:40
S63	1	"5155819".PN.	USPAT; USOCR	OR	OFF	2005/11/05 14:41
S64	1	"5386573".PN.	USPAT; USOCR	OR	OFF	2005/11/05 14:43
S65	1	"5426769".PN.	USPAT; USOCR	OR	OFF	2005/11/05 14:43
S66	1	"5493656".PN.	USPAT; USOCR	OR	OFF	2005/11/05 14:46
S67	1	"5659797".PN.	USPAT; USOCR	OR	OFF	2005/11/05 14:52
S68	1	"5659797".PN.	USPAT; USOCR	OR	OFF	2005/11/05 14:53
S69	64	"5930523"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/05 14:56